

Electronic Acknowledgement Receipt

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Application Number:	10575098
International Application Number:	
Confirmation Number:	3649
Title of Invention:	Level Shift Circuit and Semiconductor Integrated Circuit Including the Same
First Named Inventor/Applicant Name:	Tsuyoshi Matsushita
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Payment was successfully received in RAM	\$ 130
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File Listing:

Document Number	Document Description	File Name	File Size(Bytes)/ Message Digest	Multi Part /.zip	Pages (if appl.)
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1		071971_0566.pdf	326415 f2792f65cb405d45ee3efa62b768bc8a105c501a	yes	9
Multipart Description/PDF files in .zip description					
Document Description		Start	End		
Amendment/Req. Reconsideration-After Non-Final Reject		1	2		
Claims		3	3		
Applicant Arguments/Remarks Made in an Amendment		5	8		
Extension of Time		9	9		
Warnings:					
Information:					
2	Fee Worksheet (PTO-875)	fee-info.pdf	30075 e32a869ec914360f91f74e20a331bbd4beb1c0c2	no	2
Warnings:					
Information:					
Total Files Size (in bytes):			356490		
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